

### E2.1 Base-Current Bias

Note that the circuit in Fig. 6.3 is *not* a recommended bias design.

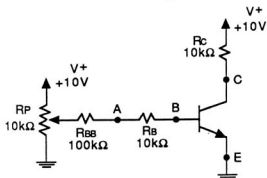


Figure 6.3 A Bad Base-Current-Biasing Circuit

Consider the fact that satisfactory operation of this circuit depends critically on  $\beta$ . As  $\beta$  varies from device to device or with temperature, the voltage  $V_{CE}$  will vary greatly, with saturation easily possible for high  $\beta$ .

### E3.1 Emitter-Current Biasing

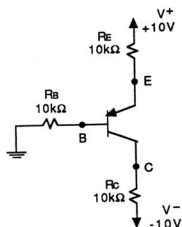


Figure 6.5 A Flexible pnp Biasing Scheme

Consider the obvious dependence of operation of the pnp on emitter current which, for the transistor in the active mode (ie, not saturated), is established by the emitter resistor and the voltage between the base and the emitter supply.

### E4.0 The BJT as Amplifier

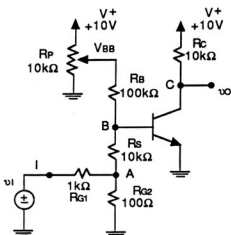


Figure 6.6 A Badly-Biased Amplifier

While the circuit shown in Fig. 6.6 uses a rather bad bias design, being a combination of base-current and base-voltage biasing, it is relatively convenient for the measurement of gain of a particular transistor under stable conditions. Incidentally, the presence of the potentiometer  $R_P$  is, generally speaking, a sure sign of less-than-ideal design.

### E4.1 Voltage Gain and Input Resistance

- Connect the circuit as shown in Fig. 6.6, and with  $v_i$  set to zero (or open), adjust  $R_P$  so that the dc voltage at C is 5 V. Then, with  $v_i$  a sine wave at 1000 Hz, and using your oscilloscope, with external triggering, to measure at nodes I and C, adjust the input signal amplitude so that  $v_o$  is a sine wave of 1 V peak amplitude. Measure the peak signals at I, A, and B, the latter being quite small.